IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:

Shunpei YAMAZAKI et al.) Art Unit: 1762

Serial No. 10/078,240 Examiner: Unassigned

Filed: 02/20/2002

METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

ANUFACTURING A

OR DEVICE

CERTIFICATE OF MAILING

Thereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 7/(2 2002.

INFORMATION DISCLOSURÉ STATEMENT AND **NOTICE OF RELATED APPLICATIONS**

Commissioner for Patents and Trademarks

Washington, D.C. 20231

Sir:

For:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.9721.9 it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The documents are being submitted within three (3) months of the filing of this application or entry into the national stage of this application, or before the first Office Action on the merits, whichever is later, therefore no fee or certification is required under 37 C.F.R § 1.97(b).

The Examiner's attention is directed to related application Serial Nos. 10/056,054 and 10/081,767. The enclosed references were cited in each of the related applications.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

Jefffey L. Costellia Reg. No. 35,483

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